Massachusetts Institute of Technology Department of Electrical Engineering and Computer Science

Proposal for Thesis Research in Partial Fulfillment of the Requirements for the Degree of Doctor of Philosophy

TITLE: SUBMITTED BY: Parallel Processor Architecture Peter Nuth 305 Memorial Drive, 606C Cambridge, MA 02139

DATE OF SUBMISSION: EXPECTED DATE OF COMPLETION: LABORATORY:

February 5, 2009 September 1990 Artificial Intelligence Laboratory

(SIGNATURE OF AUTHOR)

BRIEF STATEMENT OF THE PROBLEM:

The proposed research is a study of processor architecture for large scale parallel computer systems. The thesis introduces mechanisms for fast context switching, synchronization between tasks, and run-time binding of variable names to processor memory. Various design tradeoffs are evaluated through simulation of a processor running a typical load. This work contains estimates of the speed and complexity of the different alternatives as implemented in VLSI.

Doctoral Thesis Supervision Agreement

To: Department Graduate Committee FROM: Professor William J. Dally

The program outlined in the proposal:

TITLE:	Parallel Processor Architecture
AUTHOR:	Peter Nuth
DATE:	February 5, 2009

is adequate for a Doctoral thesis. I believe that appropriate readers for this thesis would be:

READER 1: Professor Arvind READER 2: Professor Thomas Knight

Facilities and support for the research outlined in the proposal are available. I am willing to supervise the thesis and evaluate the thesis report.

SIGNED:

Associate Professor of Electrical Engineering and Computer Science

DATE:

Doctoral Thesis Reader Agreement

To: Department Graduate Committee FROM: Professor Arvind

The program outlined in the proposal:

Parallel Processor Architecture
Peter Nuth
February 5, 2009
Professor William J. Dally
Professor Thomas Knight

is adequate for a Doctoral thesis. I am willing to aid in guiding the research and in evaluating the thesis report as a reader.

SIGNED:

Professor of Electrical Engineering and Computer Science

DATE:

Doctoral Thesis Reader Agreement

To: Department Graduate Committee From: Professor Thomas Knight

The program outlined in the proposal:

Parallel Processor Architecture
Peter Nuth
February 5, 2009
Professor William J. Dally
Professor Arvind

is adequate for a Doctoral thesis. I am willing to aid in guiding the research and in evaluating the thesis report as a reader.

SIGNED: Assistant Professor of Electrical Engineering AND COMPUTER SCIENCE

DATE:

Doctoral Thesis Reader Agreement

To: Department Graduate Committee FROM: Professor William J. Dally

The program outlined in the proposal:

TITLE:	Parallel Processor Architecture
AUTHOR:	Peter Nuth
DATE:	February 5, 2009
SUPERVISOR:	Professor William J. Dally
Other Reader:	Professor Arvind
Other Reader:	Professor Thomas Knight

is adequate for a Doctoral thesis. I am willing to aid in guiding the research and in evaluating the thesis report as a reader.

SIGNED:

Associate Professor of Electrical Engineering and Computer Science

DATE: